

PNP Silicon AF Transistor

- For AF input stages and driver applications
- High current gain
- Low collector-emitter saturation voltage
- Low noise between 30 Hz and 15 kHz
- Complementary types: BC847, BC850
- Not for automotive applications*
- Pb-free (RoHS compliant) package¹⁾



Type	Marking	Pin Configuration			Package
BC857B	3Fs	1 = B	2 = E	3 = C	SOT23
BC857C	3Gs	1 = B	2 = E	3 = C	SOT23
BC860C	4Gs	1 = B	2 = E	3 = C	SOT23

* Automotive qualification ongoing

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V_{CEO}	45	V
Collector-emitter voltage	V_{CES}	50	
Collector-base voltage	V_{CBO}	50	
Emitter-base voltage	V_{EBO}	5	
Collector current	I_C	100	mA
Peak collector current	I_{CM}	200	
Peak base current	I_{BM}	200	
Peak emitter current	I_{EM}	200	mA
Total power dissipation- $T_S \leq 119\text{ °C}$	P_{tot}	330	mW
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	-65 ... 150	

¹⁾Pb-containing package may be available upon special request

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ¹⁾	R_{thJS}	≤ 95	K/W

Electrical Characteristics at $T_A = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

DC Characteristics

Collector-emitter breakdown voltage $I_C = 10\text{ mA}, I_B = 0$	$V_{(BR)CEO}$	45	-	-	V
Collector-base breakdown voltage $I_C = 10\text{ }\mu\text{A}, I_E = 0$	$V_{(BR)CBO}$	50	-	-	
Collector-emitter breakdown voltage $I_C = 10\text{ }\mu\text{A}, V_{BE} = 0$	$V_{(BR)CES}$	50	-	-	
Emitter-base breakdown voltage $I_E = 1\text{ }\mu\text{A}, I_C = 0$	$V_{(BR)EBO}$	5	-	-	
Collector-base cutoff current $V_{CB} = 30\text{ V}, I_E = 0$ $V_{CB} = 30\text{ V}, I_E = 0, T_A = 150\text{ }^\circ\text{C}$	I_{CBO}	-	-	0.015 5	μA
DC current gain ²⁾ $I_C = 10\text{ }\mu\text{A}, V_{CE} = 5\text{ V}, h_{FE}\text{-group B}$ $I_C = 10\text{ }\mu\text{A}, V_{CE} = 5\text{ V}, h_{FE}\text{-group C}$ $I_C = 2\text{ mA}, V_{CE} = 5\text{ V}, h_{FE}\text{-group B}$ $I_C = 2\text{ mA}, V_{CE} = 5\text{ V}, h_{FE}\text{-group C}$	h_{FE}	- - 220 420	250 480 290 520	- - 475 800	-
Collector-emitter saturation voltage ²⁾ $I_C = 10\text{ mA}, I_B = 0.5\text{ mA}$ $I_C = 100\text{ mA}, I_B = 5\text{ mA}$	V_{CEsat}	- -	75 250	300 650	mV
Base emitter saturation voltage ²⁾ $I_C = 10\text{ mA}, I_B = 0.5\text{ mA}$ $I_C = 100\text{ mA}, I_B = 5\text{ mA}$	V_{BEsat}	- -	700 850	- -	
Base-emitter voltage ²⁾ $I_C = 2\text{ mA}, V_{CE} = 5\text{ V}$ $I_C = 10\text{ mA}, V_{CE} = 5\text{ V}$	$V_{BE(ON)}$	600 -	650 -	750 820	

¹⁾For calculation of R_{thJA} please refer to Application Note Thermal Resistance

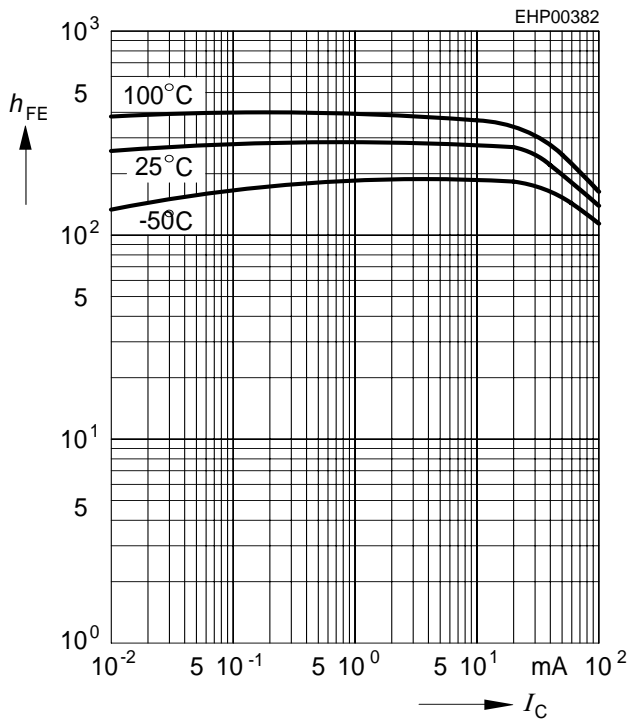
²⁾Pulse test: $t < 300\text{ }\mu\text{s}; D < 2\%$

Electrical Characteristics at $T_A = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics					
Transition frequency $I_C = 20\text{ mA}$, $V_{CE} = 5\text{ V}$, $f = 100\text{ MHz}$	f_T	-	250	-	MHz
Collector-base capacitance $V_{CB} = 10\text{ V}$, $f = 1\text{ MHz}$	C_{cb}	-	2	-	pF
Emitter-base capacitance $V_{EB} = 0.5\text{ V}$, $f = 1\text{ MHz}$	C_{eb}	-	9	-	
Noise figure $I_C = 0.2\text{ mA}$, $V_{CE} = 5\text{ V}$, $f = 1\text{ kHz}$, $\Delta f = 200\text{ Hz}$, $R_S = 2\text{ k}\Omega$, BC860	F	-	1	4	dB
Equivalent noise voltage $I_C = 200\text{ }\mu\text{A}$, $V_{CE} = 5\text{ V}$, $R_S = 2\text{ k}\Omega$, $f = 10 \dots 50\text{ Hz}$, BC860	V_n	-	-	0.11	μV

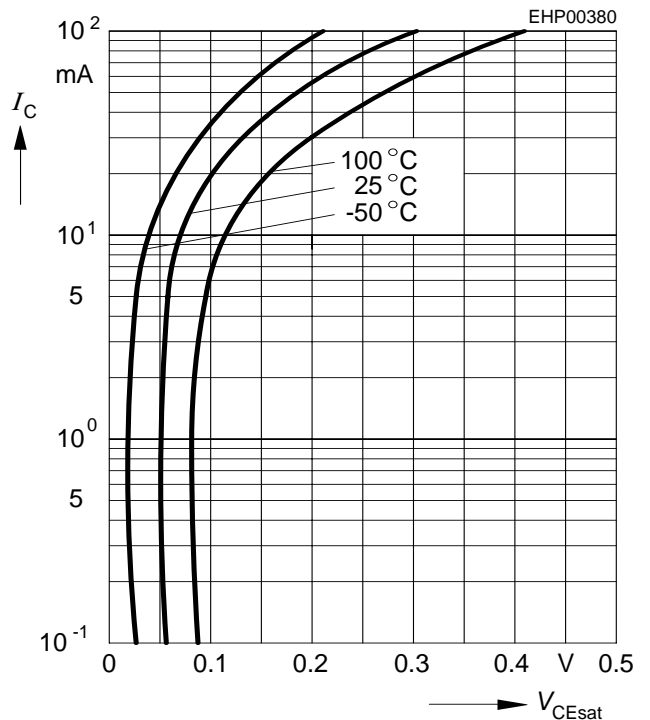
DC current gain $h_{FE} = f(I_C)$

$V_{CE} = 5\text{ V}$



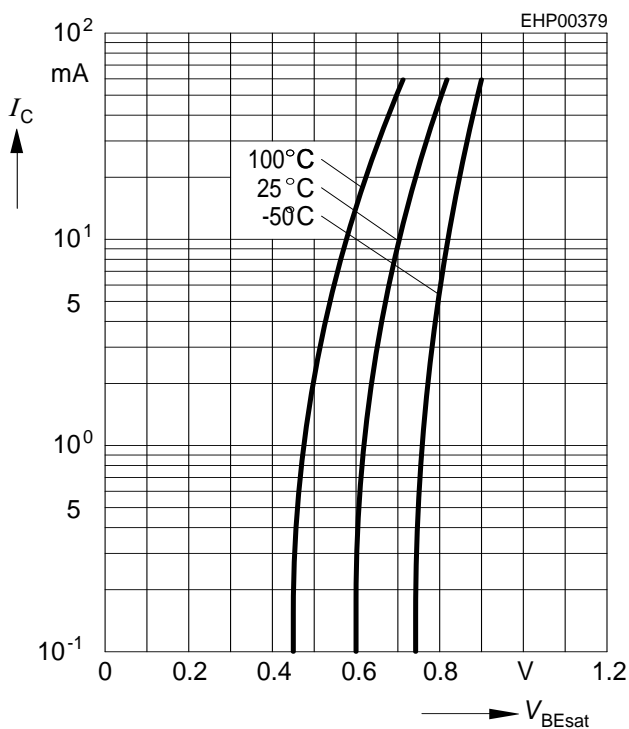
Collector-emitter saturation voltage

$I_C = f(V_{CEsat}), h_{FE} = 20$



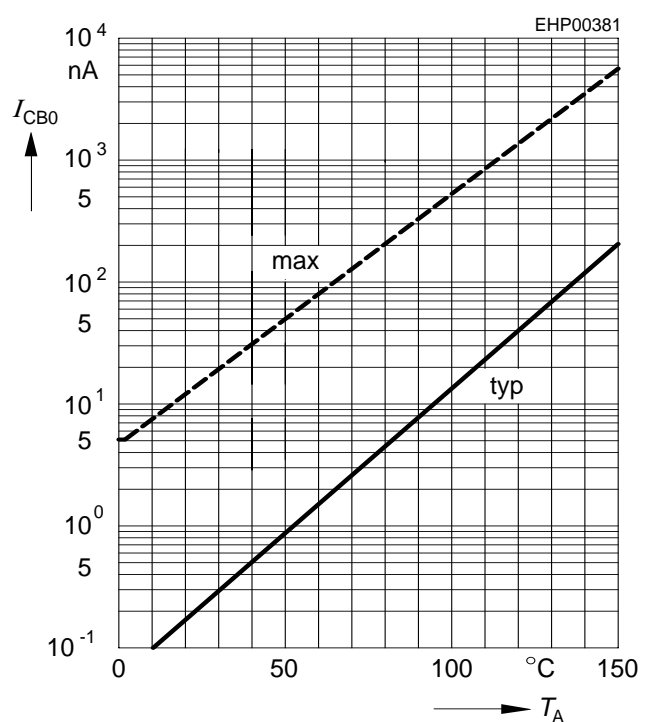
Base-emitter saturation voltage

$I_C = f(V_{BEsat}), h_{FE} = 20$



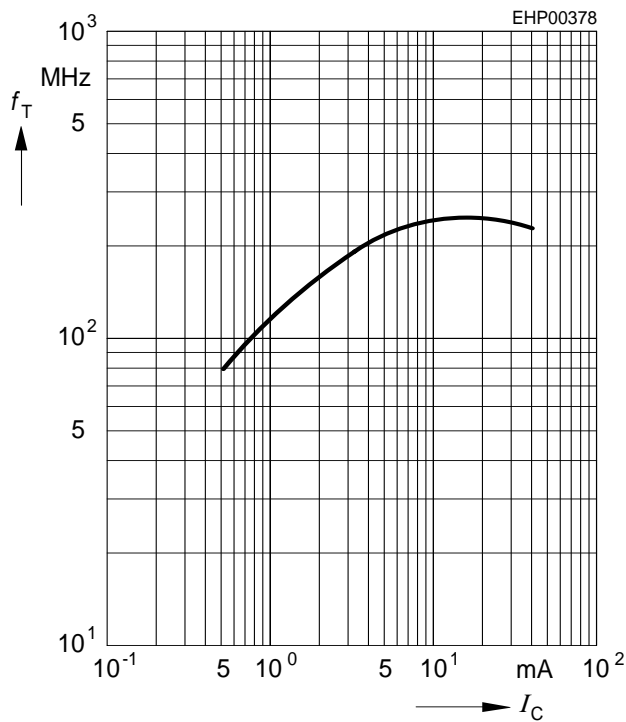
Collector cutoff current $I_{CBO} = f(T_A)$

$V_{CBO} = 30\text{ V}$



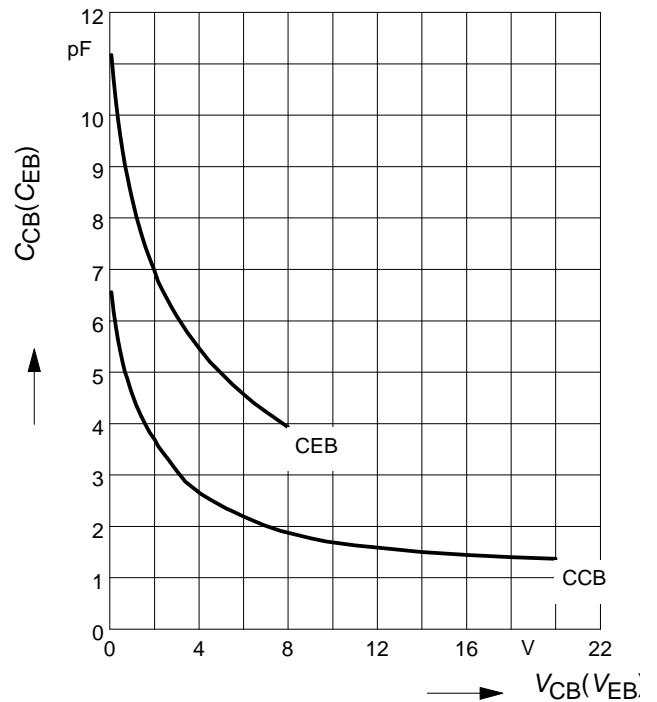
Transition frequency $f_T = f(I_C)$

$V_{CE} = 5\text{ V}$



Collector-base capacitance $C_{cb} = f(V_{CB})$

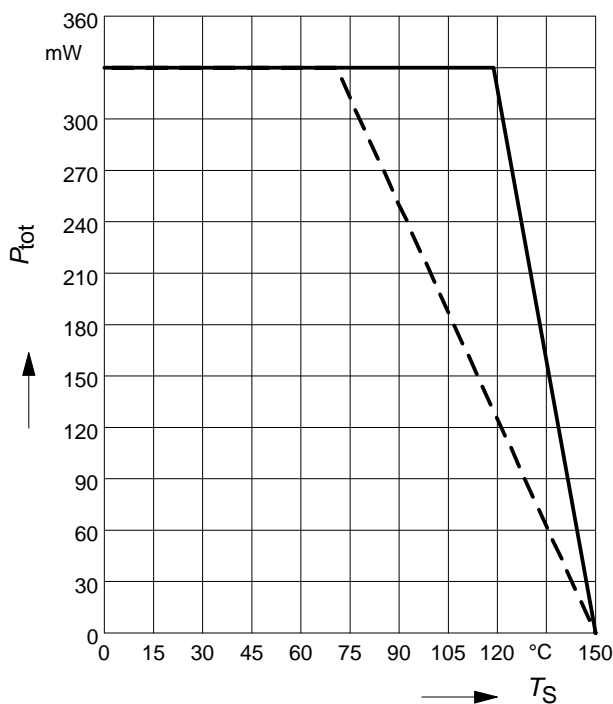
Emitter-base capacitance $C_{eb} = f(V_{EB})$



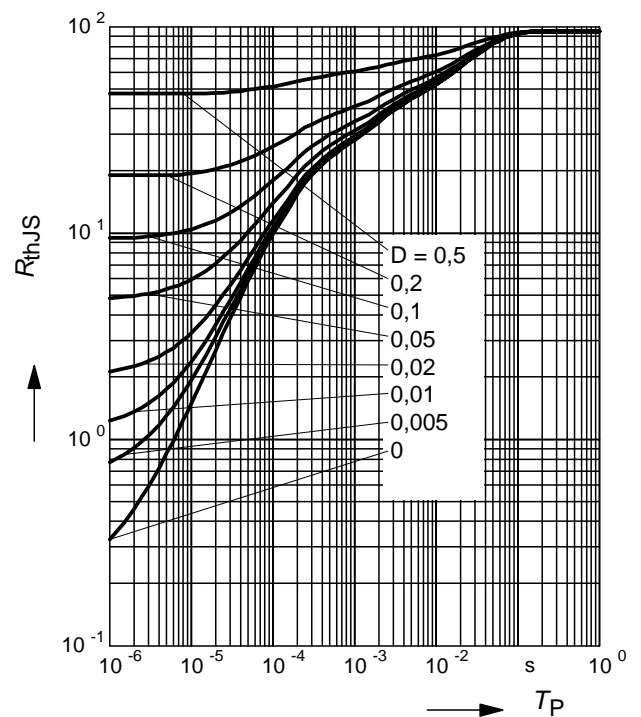
Total power dissipation $P_{tot} = f(T_S)$

— BC857...BC860B500x

... BC857...BC860Exxx (e.g. E6327)

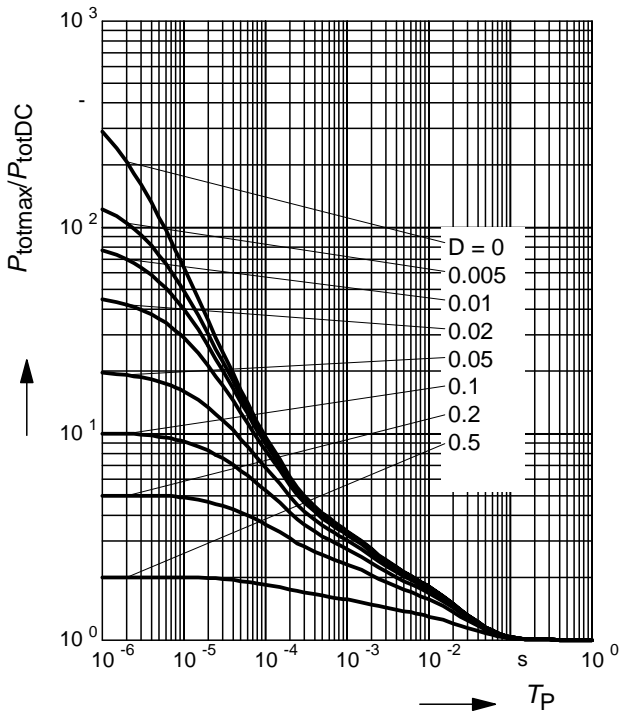


Permissible Pulse Load $R_{thJS} = f(t_p)$



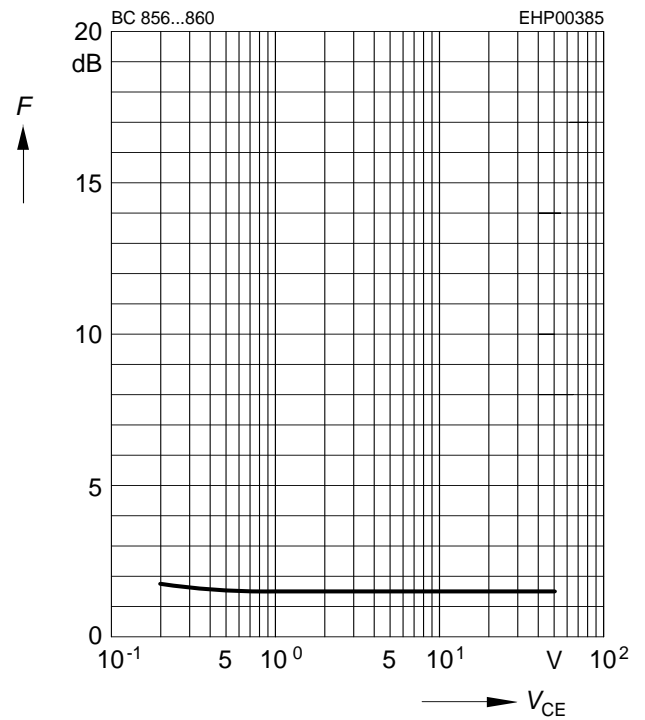
Permissible Pulse Load

$$P_{\text{totmax}}/P_{\text{totDC}} = f(t_p)$$



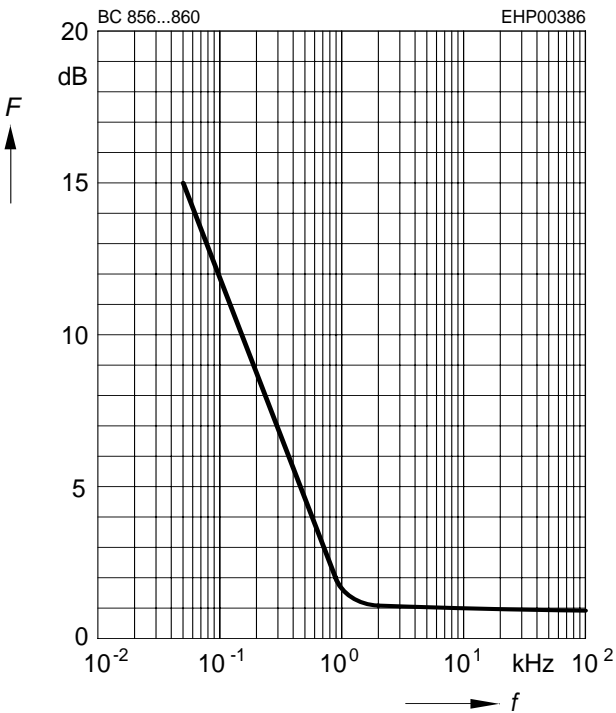
Noise figure $F = f(V_{CE})$

$$I_C = 0.2\text{mA}, R_S = 2\text{k}\Omega, f = 1\text{kHz}$$



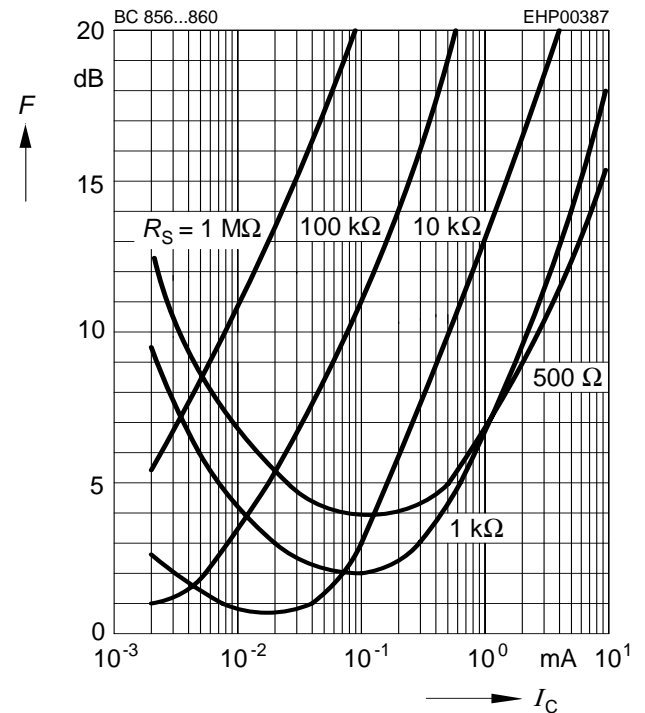
Noise figure $F = f(f)$

$$I_C = 0.2\text{ mA}, V_{CE} = 5\text{ V}, R_S = 2\text{ k}\Omega$$



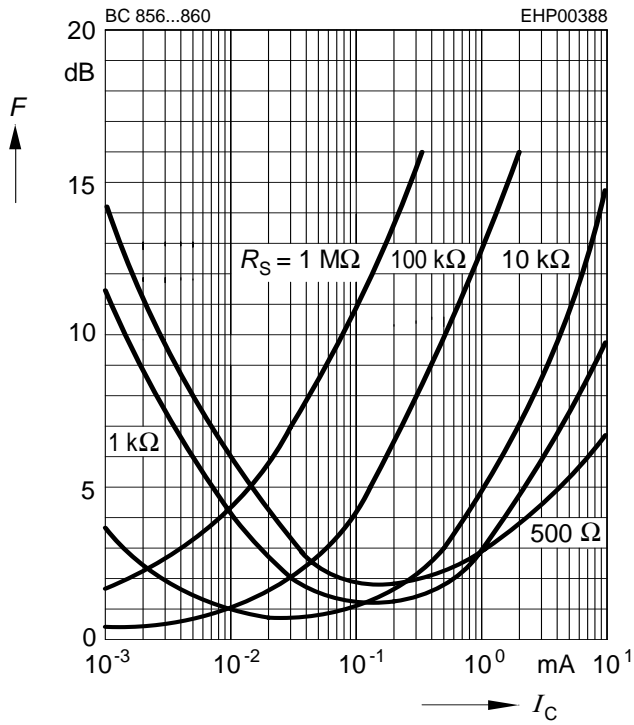
Noise figure $F = f(I_C)$

$$V_{CE} = 5\text{V}, f = 120\text{Hz}$$



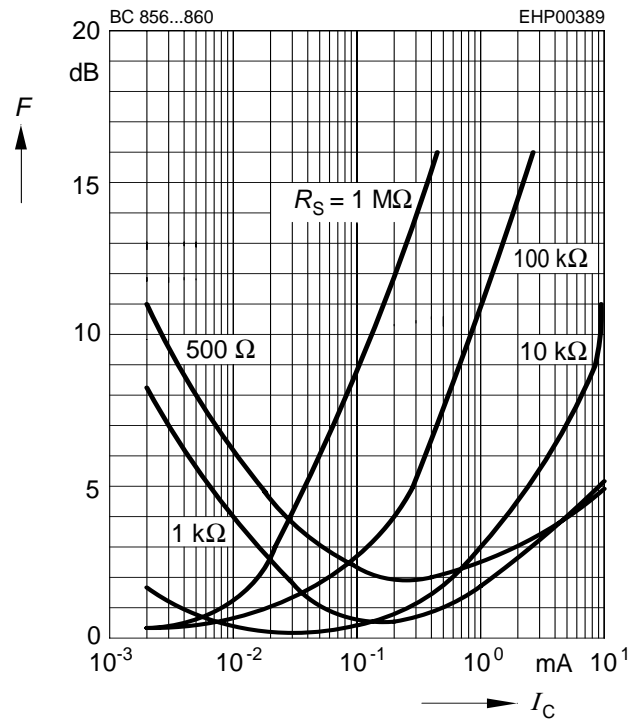
Noise figure $F = f(I_C)$

$V_{CE} = 5V, f = 1kHz$



Noise figure $F = f(I_C)$

$V_{CE} = 5V, f = 10kHz$

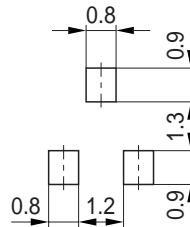


Package Outline

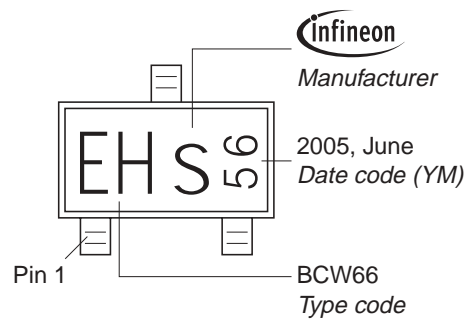


1) Lead width can be 0.6 max. in dambar area

Foot Print

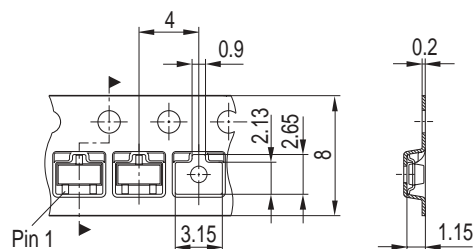


Marking Layout (Example)



Standard Packing

Reel \varnothing 180 mm = 3.000 Pieces/Reel
 Reel \varnothing 330 mm = 10.000 Pieces/Reel



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